

Multiple tasks, the single solution!



JTAG Manager Presentation

by

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The **StarTest JTAG MANAGER** software set contains three main modules:

JMAN – JTAG Manager is the PC (Windows)-based operational multi-language (English, Russian) envelope that intended for implementation on the OEM electronic board manufacturing facilities, contract manufacturers and/or in the R&D labs;

OFS – Operator's Fault Spotlight or Offline Fault Search station. This module allows to work with test history files stored by **JMAN** in order to repair fault boards offline (without connection to BS equipment) in any time;

TFL – Test Fault Locator viewer is a subset of the **JTAG Manager**. Works with BOM and can read test report files from FP, ICT and FT stations.

The software is intended for use by:

JTAG testing operators - **JMAN** and **OFS** modules;

Repair technician staff after JTAG and ICT testing - **TFL** and **OFS** modules;

Manual soldering workers - **TFL** module;

Visual inspection controllers - **TFL** module.

<http://start-test.com/Products/JTAGManager.php>

The **JTAG Manager** has easy and intuitively understandable GUI for a new project creation that allows to build tree projects structure.

The screenshot displays the 'New Project' dialog box in the JTAG Manager software. The 'Objects' pane on the left shows a tree structure with 'GE-Medical' as the root, containing 'DAB' and '5338221 Rev.5'. The 'Platforms' section has checkboxes for 'Corelis', 'onTAP', 'JT', and 'Demo', with 'Demo' selected. The 'Properties' section lists various project settings like 'Part Number' (5338221), 'Display Name' (DAB), and 'Corelis Multiboard Prefix'. The 'Files' section shows a list of files to be included in the project, including 'Layout File', 'Nails File', 'Board Schematics', 'JTAG Chains', 'BOM File', 'Log File', 'Corelis Project', and 'Demo Project'. The 'BOM Fields' dialog is also visible, showing a table of fields for the Bill of Materials (BOM) with columns for Reference, Part Number, Description, and D. The table lists 21 rows of data, including 'Report Name', 'Part Number', 'Description', 'nbr of level', 'Date', 'State', and a detailed list of components like 'DAB CTRL for U82', 'U82', 'FLASH VXWOR...', 'U70', 'FLASH RESETV...', 'FLASH BOOT for...', 'VR2', 'Fuse House for F...', and 'CTACon'.

New Project

Objects

- GE-Medical
 - DAB
 - 5338221 Rev.5

Platforms

☒ Corelis ☐ onTAP ☐ JT ☒ Demo

Properties

Name	Value
Part Number	5338221
Display Name	DAB
Corelis Multiboard Prefix	
Don't Load Nets Geometry	False
Perform Diagnostics	Always
Corelis Parameters	
Demo Parameters	

Files

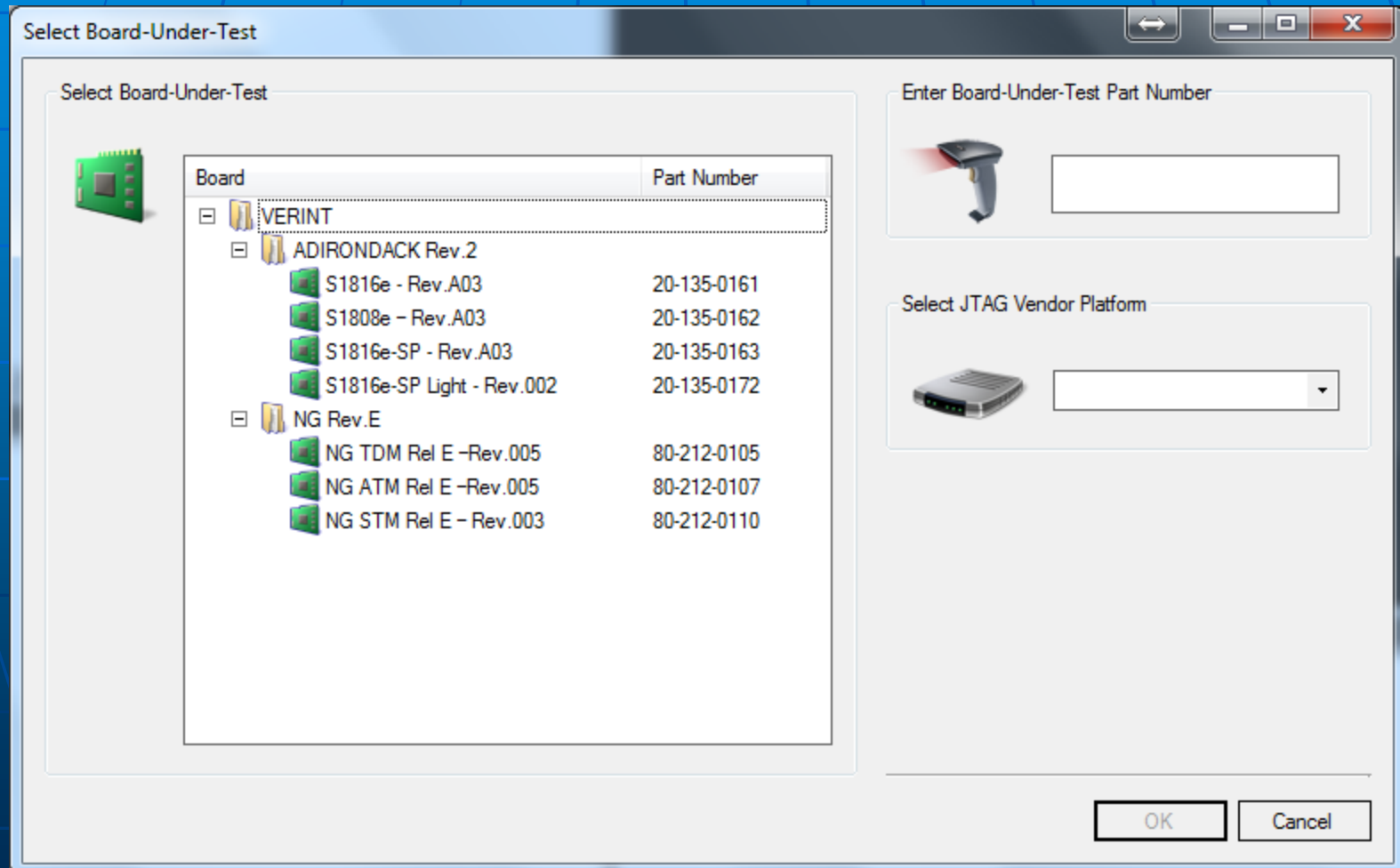
Type	Path	Embedded	Update
Layout File	D:\Projects\ICT\Flextronics\GE\ICT\DA...	Yes	Yes
Nails File		No	
Board Schematics	D:\Projects\ICT\Flextronics\GE\ICT\DA...	Yes	Yes
JTAG Chains		No	
BOM File	D:\Projects\Corelis\GE\DAB\DAB.csv	No	
Log File		No	
Corelis Project		No	
Demo Project		No	

BOM Fields

Part Number Column: B Reference Column: A Description Column: C

	Reference	Part Number	Description	D	E
1	Report Name		Multi-level Bill Of ...		
2	Part		5338221	5	
3	Description		DAB - Detector A...		
4	nbr of level		0		
5	Date:		02 June 2010		
6	State :		Release		
7					
8	Row Number	Level	Mark Number	Reference Desig...	Type
9					
10		0			CTAAss
11	1	1		DAB CTRL for U82	CTASoft
12	2	2		U82	CTAOO
13	3	1		FLASH VXWOR...	CTASoft
14	4	2		U70	CTAOO
15	5	1		FLASH RESETV...	CTASoft
16	6	2		U70	CTAOO
17	7	1		FLASH BOOT for...	CTASoft
18	8	2		U70	CTAOO
19	9	1		VR2	CTAOO
20	10	1		Fuse House for F...	CTAOO
21	11	1	20		CTACon

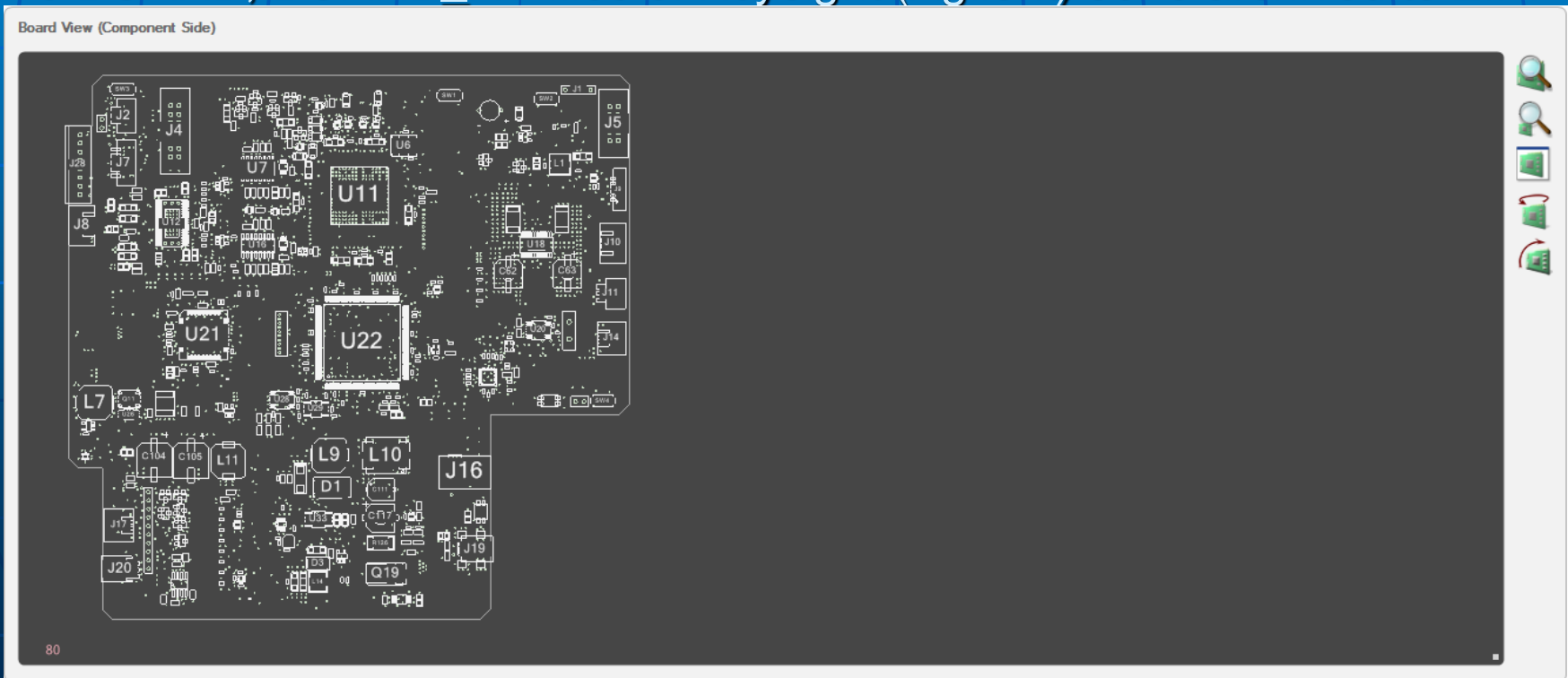
The **JTAG Manager** has simple and friendly operator oriented GUI for board to test selection and avoids necessity to search the test sequence file placement.



The **JTAG Manager** creates the Board View window from the input CAD file with convenient control panel at the right side of the window.

Supported CAD formats:

- o FABMASTER files in FAZ, FAT formats;
- o GENCAD;
- o ODB++ files archived to ZIP, TGZ;
- o BOARD, BOARD_XY files of Keysight (Agilent) 3070 archived to ZIP.



In order to reduce the project load time and the project file size the **JTAG Manager** creates file in specially designed JMD format.

Import Board Layout

Select opened layout

Layout file:

Select

Load layout from file

Layout file:

Browse

Nails file (optional):

Browse

BOM file (optional):

Browse

☐ Don't load net geometry

Load

Info

	Input Layout	JTAG Manager Database
Parts	2997	2997
Nets	2293	2293
ICT Nails	70	70
BOM Entries	142	142
File size (KB)	23,770	1,789
Load time (sec.)	8	1

Board View - mcb.jmd

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The **JTAG Manager** establishes a project database from input files CAD and BOM. The Data Base includes five interrelated tables - PARTS, NETS, ICT NAILS, BOM, CATEGORIES.

UUT Database - EPT-2015

Parts (280) Nets (122) ICT Nails (115) BOM (99) Categories (21)

Find [Deselect All](#)

Part/Pin	Net	ICT Nail	Category	BOM Part Number	BOM Description
<input type="checkbox"/> R41			Resistor	Part not in BOM	
<input type="checkbox"/> R42			Resistor	102-00029-01	RES,60K4,1%,1/20W,0201
<input type="checkbox"/> R43			Resistor	101-00227-01	RES,1.00K,1%,1/10W,0603,HV
<input type="checkbox"/> R44			Resistor	102-00029-01	RES,60K4,1%,1/20W,0201
<input type="checkbox"/> R45			Resistor	102-00031-01	RES,100K,1%,1/20W,0201
<input type="checkbox"/> R46			Resistor	102-00023-01	RES,22K,1%,1/20W,0201
<input type="checkbox"/> R47			Resistor	102-00036-01	RES,806K,1/10W,1%,0603,HV
<input type="checkbox"/> R48			Resistor	102-00025-01	RES,41K2,1%,1/20W,0201
<input type="checkbox"/> R49			Resistor	102-00014-01	RES,10R,1%,1/10W,0603,HV
<input type="checkbox"/> R50			Resistor	101-00253-01	RES Anti-Surge,20R,1%,0.2W,0603
<input type="checkbox"/> R51			Resistor	102-00026-01	RES,49K9,1%,1/20W,0201
<input type="checkbox"/> R52			Resistor	102-00025-01	RES,41K2,1%,1/20W,0201
<input type="checkbox"/> 1	B ICOMP MEAS	37			
<input type="checkbox"/> 2	N15409851	32			
<input type="checkbox"/> R53			Resistor	102-00020-01	RES,10K,1%,1/20W,0201
<input type="checkbox"/> R54			Resistor	101-00213-01	RES,13.7K,1%,1/20W,0201
<input type="checkbox"/> R55			Resistor	102-00029-01	RES,60K4,1%,1/20W,0201
<input type="checkbox"/> R56			Resistor	102-00037-01	RES,10M,1%,1/10W,0603,HV

The **JTAG Manager** allows to implement search of components by PN or by Categories.

The screenshot displays the JTAG Manager software interface, which includes a main PCB view and two floating windows for component search.

Main PCB View (Print Side): Shows a top-down view of a printed circuit board (PCB) with various components labeled, including J21, J27, J24, J25, J26, J28, J29, J30, J31, J32, J33, J34, J35, J36, J37, J38, J39, J40, J41, J42, J43, J44, J45, J46, J47, J48, J49, J50, J51, J52, J53, J54, J55, J56, J57, J58, J59, J60, J61, J62, J63, J64, J65, J66, J67, J68, J69, J70, J71, J72, J73, J74, J75, J76, J77, J78, J79, J80, J81, J82, J83, J84, J85, J86, J87, J88, J89, J90, J91, J92, J93, J94, J95, J96, J97, J98, J99, J100.

Board View (Component Side): Shows a bottom-up view of the PCB with components labeled, including U11, U21, U22, U23, U24, U25, U26, U27, U28, U29, U30, U31, U32, U33, U34, U35, U36, U37, U38, U39, U40, U41, U42, U43, U44, U45, U46, U47, U48, U49, U50, U51, U52, U53, U54, U55, U56, U57, U58, U59, U60, U61, U62, U63, U64, U65, U66, U67, U68, U69, U70, U71, U72, U73, U74, U75, U76, U77, U78, U79, U80, U81, U82, U83, U84, U85, U86, U87, U88, U89, U90, U91, U92, U93, U94, U95, U96, U97, U98, U99, U100.

UUT Database - PC000083-REV03 (Top Window): Displays a table of components with columns: Part Number/Part, Total Parts, Missing Parts, and Description. The table lists several components, including resistors and capacitors.

Part Number/Part	Total Parts	Missing Parts	Description
PP0000209	1		RES 4.9K 0.1W 1% 0603
PP0000226	10		RES 0E 0.0625W 0402
PP0000229	2		RES 330E 0.1W 1% 0603
PP0000230	2		RES 160E 0.1W 1% 0603
PP0000233	13		CAP 2.2UF CER 16V 10% X5R 0603
C11			
C64			
C70			
C83			
C169			
C190			

UUT Database - PC000083-REV03 (Bottom Window): Displays a table of components with columns: Category/Part, Total Parts, and Prefixes. The table lists various component categories and their associated prefixes.

Category/Part	Total Parts	Prefixes
Buzzers	2	BZ
Capacitor	242	C, CD
Choke	19	L
Connector	27	J, CON
Crystal	2	X, XT, OSC, Y
Diode	45	D, CR, DN
Fiducial	0	FD, FF
Fuse	1	F, FS
Integrated Circuit	53	IC, U, UV, U1D, V
LED	0	LD
Mechanical	0	H, FX, MH
Potentiometer	0	VR, P, RV
Relay	0	RL, K
Resistor	427	R, RT
Resistor Network	0	RN, RM
Strap	0	W
Switch	11	S, SW
Test Point	30	TP, A, AT
Transformer	0	TR
Transistor	32	T, Q, TS
Zener	0	Z

The **JTAG Manager** realizes simultaneously search of Parts and Nets in Board Scheme and Board View windows

The screenshot displays the JTAG Manager software interface with three main windows:

- Test Fault Locator - [PC000083-REV03]**: The main application window with a menu bar (File, Project, Reports, Windows, Help) and a toolbar.
- Board View (Print Side)**: A large window showing a detailed PCB layout with various components labeled (e.g., J21, J27, J24, J25, U41, Y2, D13, D23, D24, D25, D26, D27, D28, D29, D30, D31, D32, D33, D34, D35, D36, D37, D38, D39, D40, D41, D42, D43, D44, D45, D46, D47, D48, D49, D50, D51, D52, D53, D54, D55, D56, D57, D58, D59, D60, D61, D62, D63, D64, D65, D66, D67, D68, D69, D70, D71, D72, D73, D74, D75, D76, D77, D78, D79, D80, D81, D82, D83, D84, D85, D86, D87, D88, D89, D90, D91, D92, D93, D94, D95, D96, D97, D98, D99, D100).
- UUT Database - PC000083-REV03**: A window showing a table of parts and nets. The table has columns: Part/Pin, Net, ICT Nail, Category, BOM Part Number, and a status column. The table lists several resistors (R327, R328, R329, R330, R331, R332, R333, R334) and their corresponding BOM part numbers. R333 is highlighted.
- Board Scheme**: A window showing a schematic diagram of the circuit. It includes a search panel on the left and a schematic diagram on the right. The search panel shows the search criteria: "Looking For: R333 in the current document" and "Results: 1 document(s) with 1 instance(s)". The schematic diagram shows a circuit with components like Q15 (FDM338P), R336 (100K), Q13 (FDM335N), and R333 (100E) connected to an LED_R.

Red arrows indicate the search results being displayed in the Board Scheme window, specifically pointing to the R333 component in the schematic diagram.

The **JTAG Manager** allows to run BS testing sequences based on CORELIS and JTAG TECHNOLOGIES platforms and graphically displays the test results on the Board View window by colors - green (Passed) and red (Failed).

JTAG Manager - [201587_rev25.jtm]

File Project Test Reports History Windows Help

Open Board and Platform 201587_Rev25 Corelis Run **FAILED** 17 seconds

Boundary-Scan (JTAG) Tests, Flash Programming & ISP for CPLD / FPGA

Test ID	Test Name	Result
11	201587_Rev25_flash_U24.fpi	PASSED
12	201587_Rev25_PLL_U35_ct.cvf	FAILED
13	201587_Rev25_ADC_U36_ct.cvf	FAILED
14	201587_Rev25_ADC_U37_ct.cvf	FAILED
15	201587_Rev25_ADC_U38_ct.cvf	FAILED
16	201587_Rev25_ADC_U39_ct.cvf	FAILED
17	201587_Rev25_ADC_U40_ct.cvf	FAILED
18	201587_Rev25_ADC_U41_ct.cvf	FAILED
19	201587_Rev25_ADC_U42_ct.cvf	FAILED
20	201587_Rev25_ADC_U43.ct.cvf	PASSED

Test Vectors - 201587_Rev25_flash_U24.fpi

Net/Pin	Vector

Board View (Component Side)

Run Log

Deselect All Entities

Test passed

Run Log Diagnostic Info

The **JTAG Manager** makes the Test Operator's work and troubleshooting process easier by concentrating all the necessary data on a single screen and connecting their by hyperlinks.

The screenshot displays the JTAG Manager software interface for the SLM-1379-V2 board. The interface is divided into several panes:

- Test Results (Left):** A list of 10 tests with their status:
 - 1 Test-infra: PASSED
 - 2 PLD - cpld_u39_erase: PASSED
 - 3 Ctrl - PAUSE: DONE
 - 4 Test-infra: PASSED
 - 5 Test-interconnect: FAILED
 - 6 Test-pull_resistors: FAILED
 - 7 Test-u38_i2c: PASSED
 - 8 Test-u48_i2c: PASSED
 - 9 Test-u68_i2c: PASSED
 - 10 Test-u68-u67_i2c: PASSED
- Board View (Center):** A component-side view of the SLM-1379-V2 board. A specific net, NW_SFP_TXFAULT_PORT0, is highlighted in orange. Below the board view is a 'Diagnostic Info' section showing details for the selected net.

Diagnostic Info:

```
-- Single net
SLM-1379-V2 NW_SFP MABS P
-- Single net
SLM-1379-V2 NW_SFP RXKLOS
-- Single net
SLM-1379-V2 NW_SFP TXFAULT
```
- Search (Right):** A search pane showing results for 'NW_SFP_TXFAULT_PORT0'. It indicates 1 document(s) with 3 instance(s). Below the search results is a list of parts and nets.

Parts (3065) Nets (2390)

Net/Pin:

 - ☐ NW_SFP_TX_N2
 - ☐ NW_SFP_TX_N3
 - ☐ NW_SFP_TX_N4
 - ☐ NW_SFP_TX_N5
 - ☐ NW_SFP_TX_P0
 - ☐ NW_SFP_TX_P1
 - ☐ NW_SFP_TX_P2
 - ☐ NW_SFP_TX_P3
 - ☐ NW_SFP_TX_P4
 - ☐ NW_SFP_TX_P5
 - ☐ NW_SFP_TXDIS_P...
 - ☐ NW_SFP_TXDIS_P...
 - ☐ NW_SFP_TXDIS_P...
 - ☐ NW_SFP_TXDIS_P...
 - ☒ NW_SFP_TXFAULT...
 - ☐ U39.J15
 - ☐ CON92.A2
 - ☐ R1007.1
- Board Scheme (Far Right):** A schematic view of the board showing various components and their connections. A search bar at the top indicates 'Looking For: NW_SFP_TXFAULT_PORT0 in the current document'.

The **JTAG Manager** allows to see full path of “Merged_Nets” through transparent components view, include all related printed conductors and pins.

Boundary-Scan (JTAG) Tests, Flash Programming & ISP for CPLD / FPGA

7	BA0086_BA0087_interconnect_ic.cvf	PASSED
8	BA0086_BA0087_buswire_bus.cvf	PASSED
9	FPGA_RESET.svf	PASSED
10	BA0086_BA0087_pu&pd_pull.cvf	PASSED
11	DDR3_SDRAM_BA0086-01_U1_mct.cvf	PASSED
12	DDR3_SDRAM_BA0086-01_U2_mct.cvf	PASSED
13	DDR3_SDRAM_BA0086-01_U3_mct.cvf	FAILED

Test Vectors - BA0086_BA0087_interconnect_ic.cvf

Net/Pin	Vector
✓ \$2N3197	
→ U11.K28	
→ U44.A19	
→ U44.A19	
R984.1	
TP532.1	
R165.2	
R194.2	
R861.2	
R984.2	
✓ \$2N4117	
→ U11.K23	
R694.1	
✓ \$2N4285	

Transparent resistor R984.

Board View (Component Side)

Diagnostic Info

Deselect All Entities

The **JTAG Manager** allows to find Parts shared by two or more Nets.
This feature may be helpful for shorts search.

The screenshot displays the JTAG Manager software interface. On the left, a window titled "UUT Database - BA0086_BA0087" is open. It features tabs for "Parts (3430)", "Nets (1834)", "ICT Nails (0)", "BOM (220)", and "Categories (0)". The "Find" field contains "gnd". Below this, a list of "Net/Pin" and "ICT Nails" is shown. The "Net/Pin" list includes various power and ground nets, with "+2.5V_P4080_LVDD" selected. At the bottom of this window, a section titled "Parts shared by 2 nets (+2.5V_P4080_LVDD, GND)" lists 15 parts, all of which are checked. A mouse cursor is hovering over the "Select All" button. On the right, a PCB layout view is shown, highlighting the selected parts (RN510, C1050, C1070, RN512, RN513, C1010, C1012, C1013, C980) in yellow. The layout also shows various resistors (R841, R842, R843, R844, R845, R846, R847, R859, R860, R861, R862, R742, R768, R816, R818, R877, R872, R936, R937, R938, R991, R992, R993) and capacitors (C1115, C1098, C997, C1008, C1011, C976). A "Deselect All Entities" button is located at the bottom right of the PCB view.

UUT Database - BA0086_BA0087

Parts (3430) Nets (1834) ICT Nails (0) BOM (220) Categories (0)

Find Find Shared Parts | Deselect All

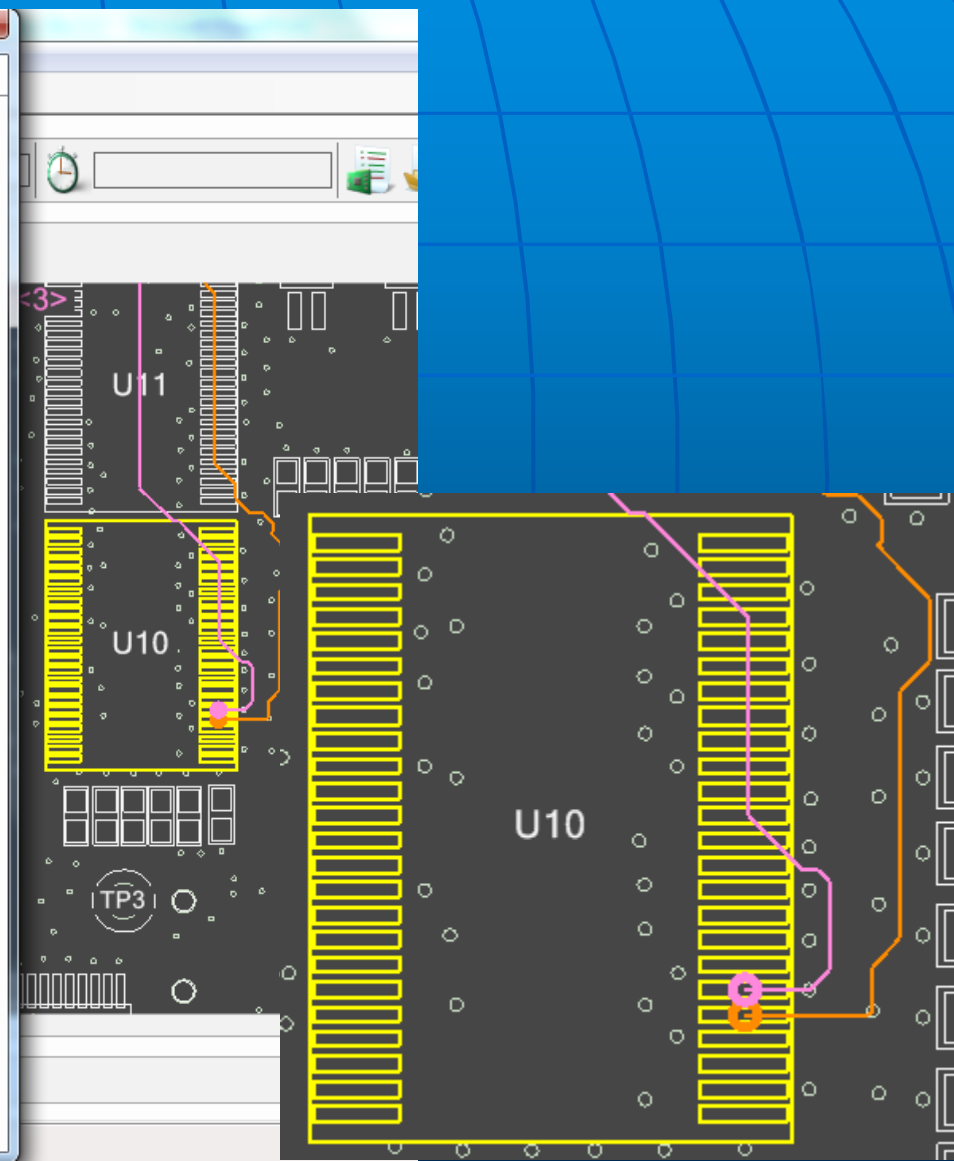
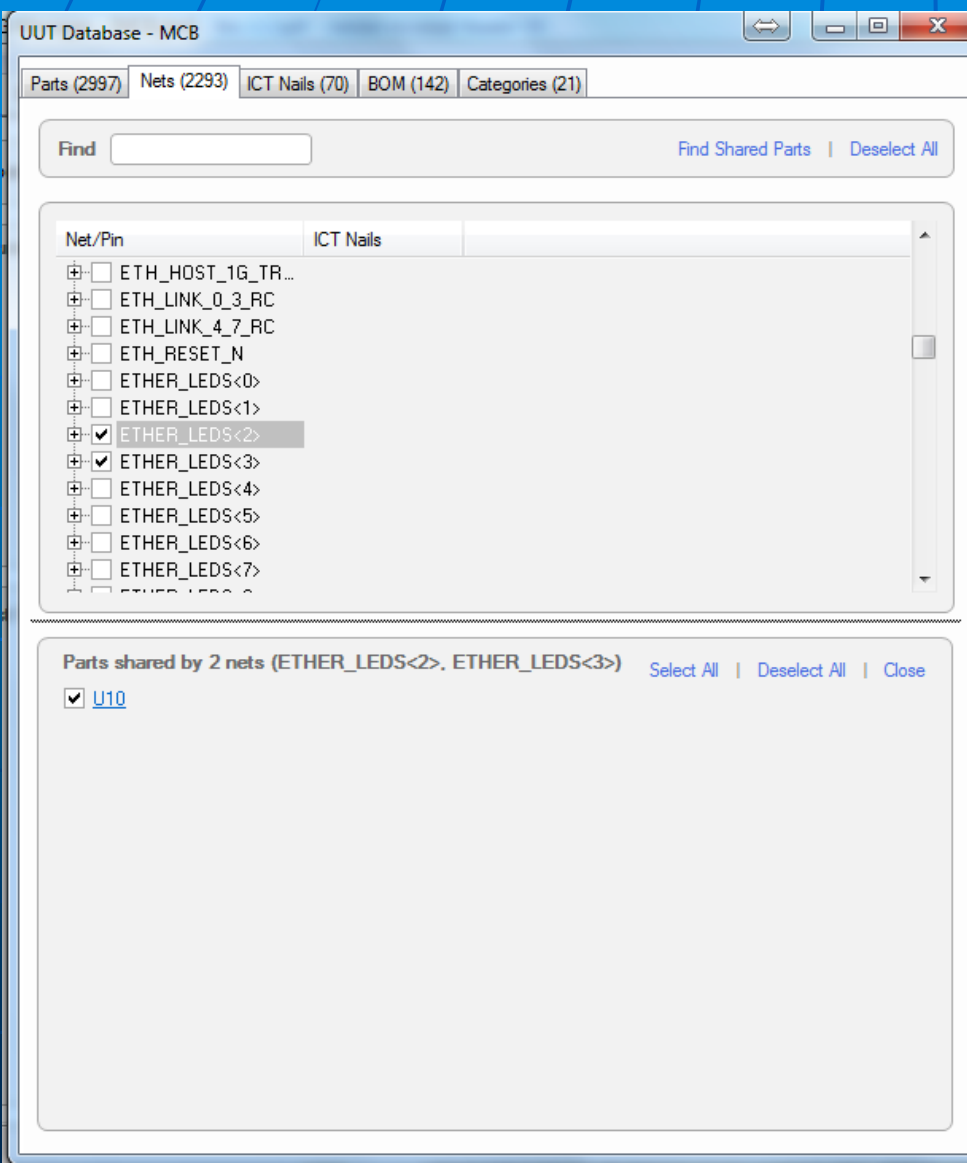
Net/Pin	ICT Nails
<input type="checkbox"/> +1.5_P4080_POVDD	
<input type="checkbox"/> +1.5V	
<input type="checkbox"/> +1.5V_DSP2_SXPV...	
<input type="checkbox"/> +1.5V_P4080_XVDD	
<input type="checkbox"/> +1.5V_P4080_XVDD...	
<input type="checkbox"/> +2.5V	
<input checked="" type="checkbox"/> +2.5V_P4080_LVDD	
<input type="checkbox"/> +3.3V	
<input type="checkbox"/> +5V2	
<input type="checkbox"/> +12V	
<input type="checkbox"/> +48VDC	
<input type="checkbox"/> +48VDC_GND	
<input type="checkbox"/> 1588_CLKOUT	
<input type="checkbox"/> 1588_PULSE_OUT	
<input type="checkbox"/> 1588_PULSE_OUT	

Parts shared by 2 nets (+2.5V_P4080_LVDD, GND) [Select All](#) | [Deselect All](#) | [Close](#)

- ☒ U11
- ☒ C1031
- ☒ C1010
- ☒ C942
- ☒ C941
- ☒ C978
- ☒ C940
- ☒ C956
- ☒ C1070
- ☒ C977
- ☒ C1008
- ☒ C1011
- ☒ C976

Deselect All Entities

Simultaneous highlighting of two suspicious conductors can detect the exact short location.



The **JTAG Manager** produces reports and history files with the names defined by template and stores them in the user defined folders.

☒ Save each report in an individual file

Report format:

CSV

Folder:

D:\Boards\45\45_ADL_CONT\Test_Reports

Browse

File name template:

_UUT_NAME_ _PN_ _SN_ _OPERATOR_ _STATUS_ _DATE_ _TIME_

Available keywords:

_UUT_NAME_ _PN_ _SN_ _OPERATOR_ _STATUS_ _DATE_ _TIME_

File name preview:

Motherboard 712-654321 123456 John Smith Passed 2015-10-20 14:58:56.csv

Project Name: NRB

Start Time: 12/8/2015 4:56 PM

End Time: 12/8/2015 4:59 PM

Elapsed Time: 3 minutes 5 seconds

Created by: JTAG Manager rev. 3.1 build 5818JFTE

JTAG System: JT

JTAG Controller Type: 37x7 USB

UUT P/N: NRB

UUT S/N: 0083

Sequence Status: Passed

Test Step Num	Test Step Type	Test Step Name	Elapsed time	Result
1	Ctrl	Ctrl - PAUSE	4 seconds	Done
2	Test	Test - nrb_inter - capture	0 seconds	Passed
3	Test	Test - nrb_inter - ident	0 seconds	Passed
4	STAPL	STAPL - nrb_fpga_prog_u618 - NRB_FPGA_U618	10 seconds	Passed
5	STAPL	STAPL - nrb_fpga_prog_u618 - NRB_FPGA_U618	6 seconds	Passed
6	Ctrl	Ctrl - PAUSE	25 seconds	Done
7	Test	Test - nrb_inter - capture	1 second	Passed
8	Test	Test - nrb_temp_sens20+ _u619	1 second	Done
9	Ctrl	Ctrl - IF (item=fail)		Skipped
10	Test	Test - nrb_temp_sens20- _u619	0 seconds	Passed
11	Ctrl	Ctrl - ELSE		Skipped
12	Ctrl	Ctrl - ENDIF		Skipped
13	Test	Test - nrb_temp_sens20+ _u620	1 second	Done

The **JTAG Manager** establishes a Test History Data Base from stored history files and previously repair reports to following data processing and boards troubleshooting at **Offline Fault Search (OFS)** station.

History - From Folder C:\JTAG\GMI\NRB\History

☒ Apply Filters
 ☒ Reset Filters
 ☒ Reload Results

Filter by serial numbers Clear

Filter by dates All | Today | This Week | This Month

Minimum to Maximum

Filter by statuses Select All | Deselect All

☒ Failed
☒ Aborted
☒ Passed

Filter by UUT Select All | Deselect All

☒ NRB NRB

UUT Name	Part Number	Serial Number	Platform	Instrument	End Time	Elapsed Time
NRB	NRB	0080	JT	37x7 USB	12/10/2015 12:40:15 PM	3 seconds
NRB	NRB	0037	JT	37x7 USB	12/10/2015 12:36:38 PM	4 seconds
NRB	NRB	0024	JT	37x7 USB	12/10/2015 12:31:40 PM	4 seconds
NRB	NRB	0024	JT	37x7 USB	12/10/2015 12:30:57 PM	4 seconds
NRB	NRB	0090	JT	37x7 USB	12/10/2015 12:04:40 PM	1 second
NRB	NRB	0090	JT	37x7 USB	12/10/2015 12:03:56 PM	2 minutes 41 seconds
NRB	NRB	0090	JT	37x7 USB	12/10/2015 12:00:47 PM	2 seconds
NRB	NRB	0090	JT	37x7 USB	12/10/2015 12:00:37 PM	1 minute 22 seconds
NRB	NRB	0019	JT	37x7 USB	12/10/2015 11:44:10 AM	2 minutes 46 seconds
NRB	NRB	0019	JT	37x7 USB	12/10/2015 11:41:11 AM	13 seconds
NRB	NRB	0019	JT	37x7 USB	12/10/2015 11:40:50 AM	47 seconds
NRB	NRB	0036	JT	37x7 USB	12/10/2015 11:27:32 AM	5 seconds
NRB	NRB	0036	JT	37x7 USB	12/10/2015 11:27:12 AM	5 seconds
NRB	NRB	0002	JT	37x7 USB	12/8/2015 5:11:00 PM	3 seconds
NRB	NRB	0006	JT	37x7 USB	12/8/2015 5:04:45 PM	3 seconds
NRB	NRB	0006	JT	37x7 USB	12/8/2015 5:04:23 PM	4 seconds
NRB	NRB	0006	JT	37x7 USB	12/8/2015 5:03:32 PM	3 seconds
NRB	NRB	0006	JT	37x7 USB	12/8/2015 5:03:01 PM	4 seconds
NRB	NRB	0083	JT	37x7 USB	12/8/2015 4:59:17 PM	3 minutes 6 seconds
NRB	NRB	0034	JT	37x7 USB	12/7/2015 12:32:46 PM	3 seconds
NRB	NRB	0034	JT	37x7 USB	12/7/2015 12:32:10 PM	15 seconds
NRB	NRB	0026	JT	37x7 USB	12/6/2015 8:33:34 AM	3 seconds

The stored history files include all information and screens appearing during test execution and can be viewed at **Offline Fault Search (OFS)** station for troubleshooting.

Operator Fault Spotlight - [PC1736-10 - Run Results From Monday, December 14, 2015 2:00:57 PM]

File Project Reports History Windows Help

Open BA0077-01 Rev.07 Corelis **FAILED** 4 seconds

Boundary-Scan (JTAG) Tests, Flash Programming & ISP for CPLD / FPGA

Test File	Result
1 PC1736-10_RevA_Var1_infrastructure_inf.cvf	PASSED
2 PC1736-10_RevA_Var1_interconnect_ic.cvf	PASSED
3 PC1736-10_RevA_Var1_pu&pd_pull.cvf	FAILED
4 PC1736-10_RevA_Var1_flash_U11.fpi	PASSED

Test Vectors - PC1736-10_RevA_Var1_pu&pd_pull.cvf

Net/Pin	Vector
U32.V2	
U32.V2	
U32.Y14	
C45.2	
R44.2	
U9.12	
SPI_EN.PLL_80M	
U32.V3	
U32.V3	
U32.AB20	
C170.2	
U24.16	
R206.2	

Board View (Component Side)

Diagnostic Info

Pullup/down test <PC1736-10_RevA_Var1_pu&pd_pull.cvf>
Fault detected on net SPI_EN.PLL_80M

Possible faults:

Pullup resistor on net R206.2
Another pin on this net is i

Tested pins on net SPI_EN.PLL_80M

Board Scheme

Search

Looking For:
R206 in the current document

Results:
1 document(s) with 1 instance(s)

New Search

Results:

R206 4.75Kohm 2 2R110

☒ Collapse file paths

[Show Fewer Options](#)

[Save and View this PDF in Adobe](#)

[Find a word in the current](#)

33.10 x 23.38 in

After the board repair the Repair Report should be filled in by the technician person. The Repair Report has saved as part of the history Data Base for statistical collection and further processing.

The image displays two instances of the 'Repair Report' dialog box, illustrating the data entry process for a board repair.

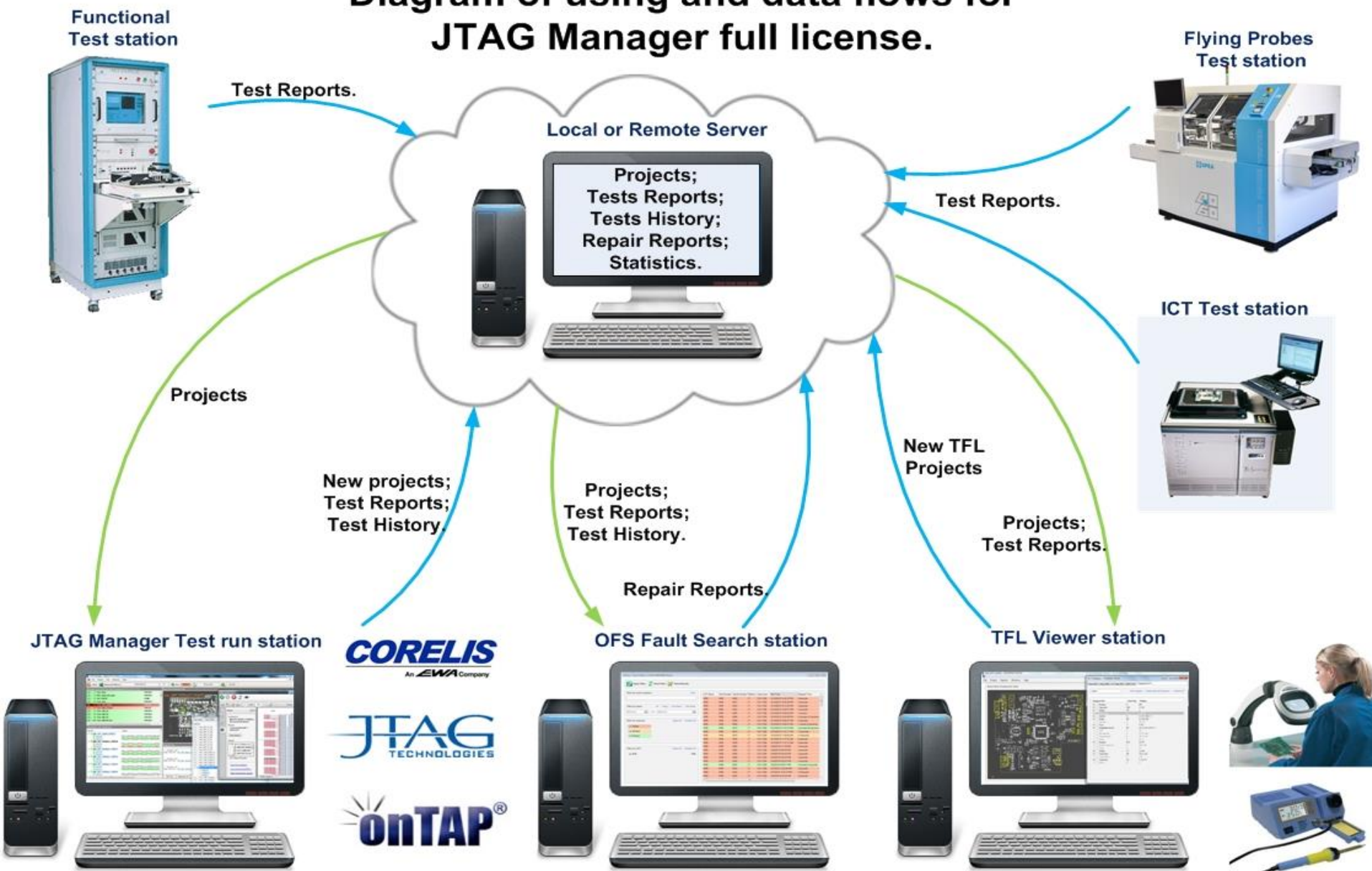
Left Dialog Box:

- Date:** 10/12/2015
- Operator:** (empty)
- Defect type:** Open (dropdown menu is open showing options: Open, Short, Missing Part, Misoriented Part, Damaged Part, Wrong Part)
- Defect sources:** (empty)
- Buttons:** Delete, Add
- Notes:** (empty text area)
- Footer:** OK, Cancel

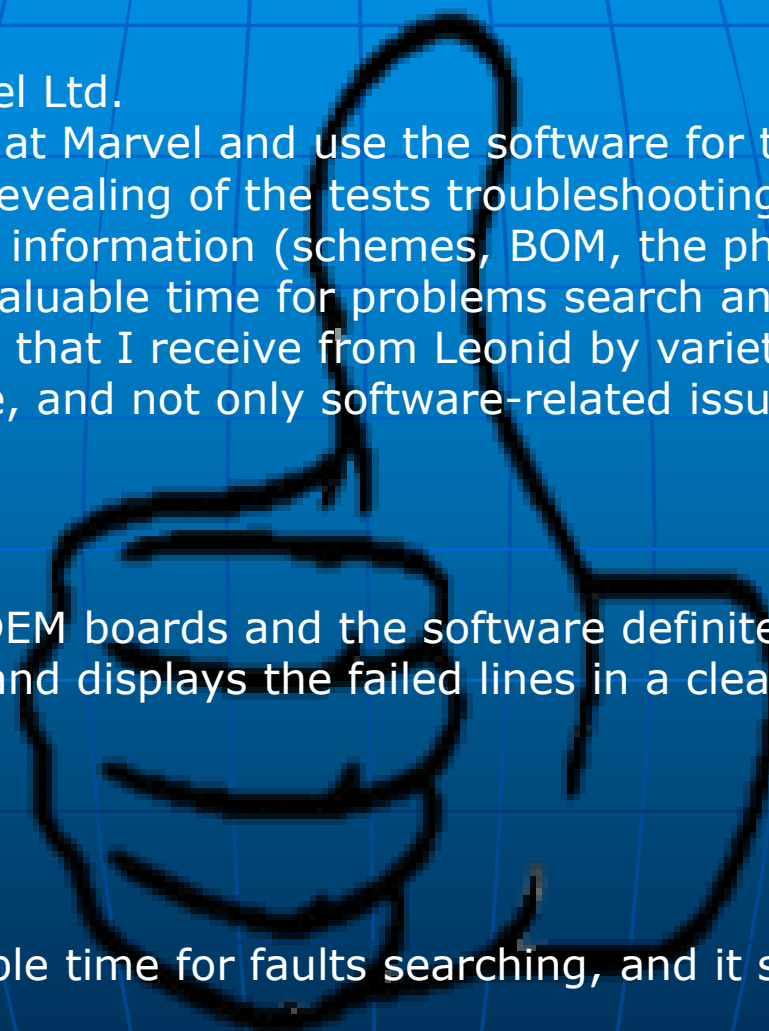
Right Dialog Box:

- Date:** 10/12/2015
- Operator:** John Smith
- Defect type:** Open
- Defect sources:** Pin U20.D1, Pin
- Buttons:** Delete, Add
- Notes:** Not soldered U20 pin 4.
- Footer:** OK, Cancel

Diagram of using and data flows for JTAG Manager full license.



User's comments (Eng)



Bialer Ran

Final tests at Marvell Israel Ltd.

My name is Ran, working at Marvel and use the software for the last year. The user-friendly software makes revealing of the tests troubleshooting very easy to process. Concentration of required information (schemes, BOM, the physical location of components etc.) saves valuable time for problems search and fixing. It is important to note the broad support that I receive from Leonid by variety of topics related to testing using the software, and not only software-related issues.

David Shabo

JTAG testing technician.

Elbit Systems Ltd.

I worked to repair of MODEM boards and the software definitely helped me to do it. The software is effective and displays the failed lines in a clear and easy form to diagnostic.

Oded Nir

Testing team leader.

Elbit Systems Ltd.

The software saves valuable time for faults searching, and it seems that it really streamlines the work.

Licenses

- 30-days free software evaluation license for a single position;
- A single position annual license. This license should be renewed each additional year;
- A single position license without time limit. Including support for the first year. The support issue is renewable for each additional year;
- Full license includes all three modules JMAN, TFL and OFS;
- TFL license includes support of TFL module only;
- OFS license includes support of OFS module only.
- Corporative license for up to 10 JMAN and 10 TFL or OFS stations;
- Corporative license for up to 20 JMAN and 20 TFL or OFS stations;
- Corporative license for up to 50 JMAN and 50 TFL or OFS stations.

**Thank you
for your attention.**